

It is to be understood that the present invention is not limited to the specific details shown and described herein, but may be embodied in other forms without departing from the spirit and scope of the invention.

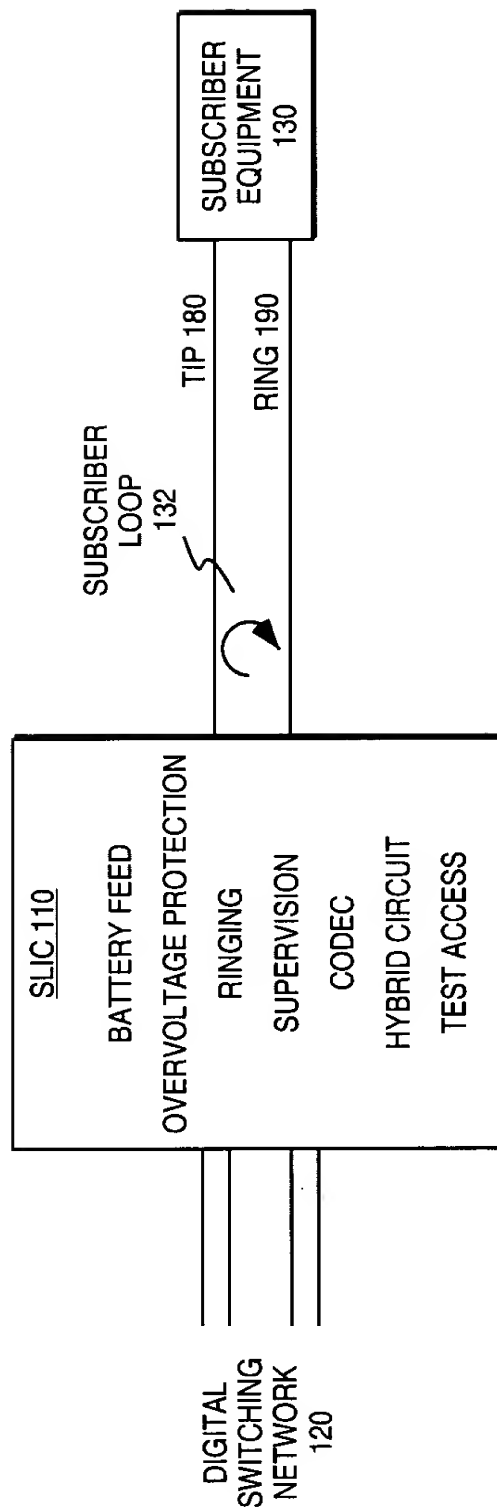


FIG. 1

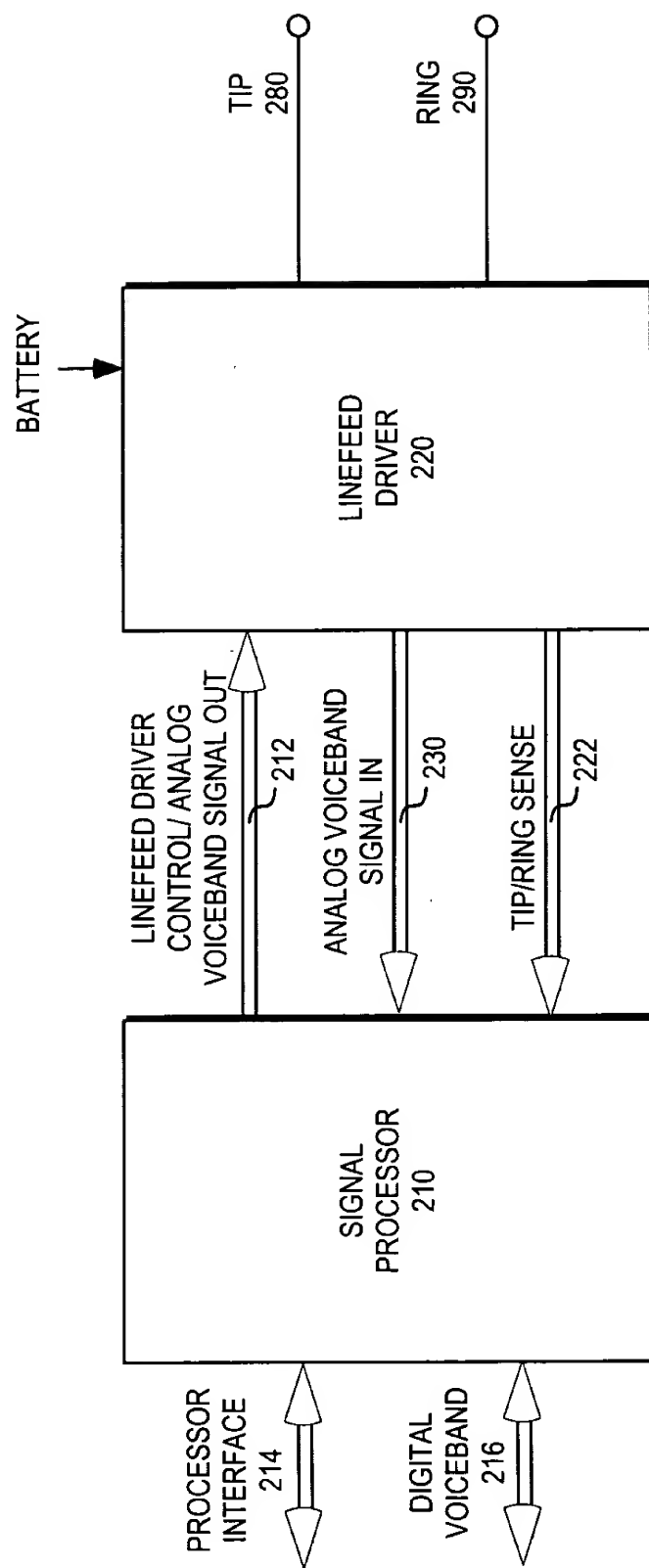


FIG. 2

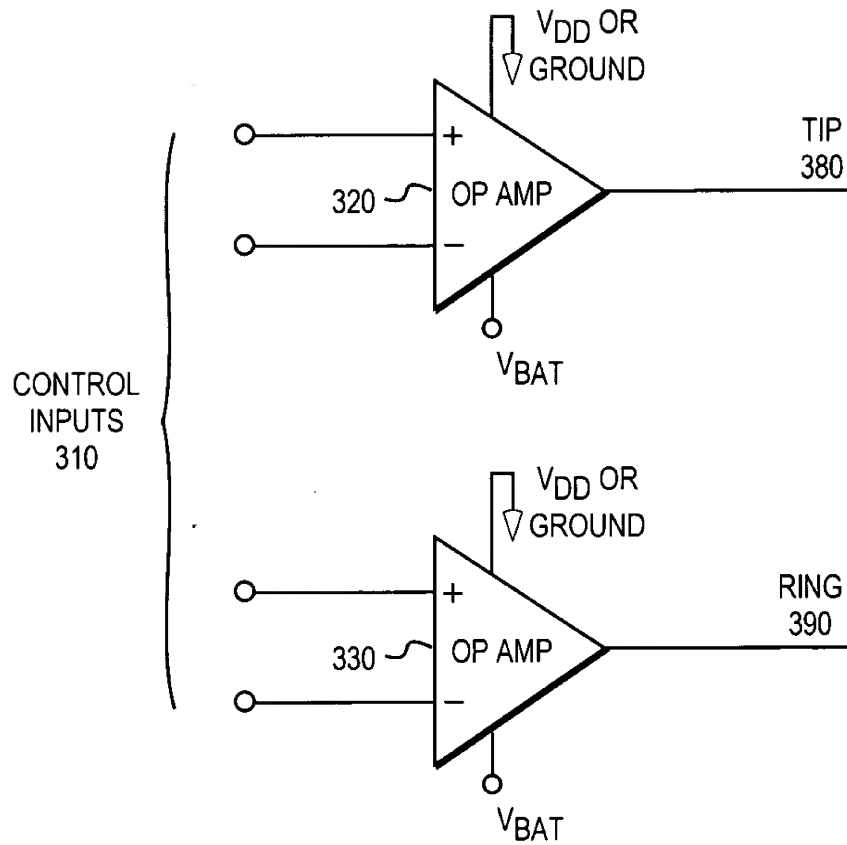


FIG. 3

FIG. 4 is a schematic diagram of a linefeed control circuit.

TO SIGNAL PROCESSOR 210

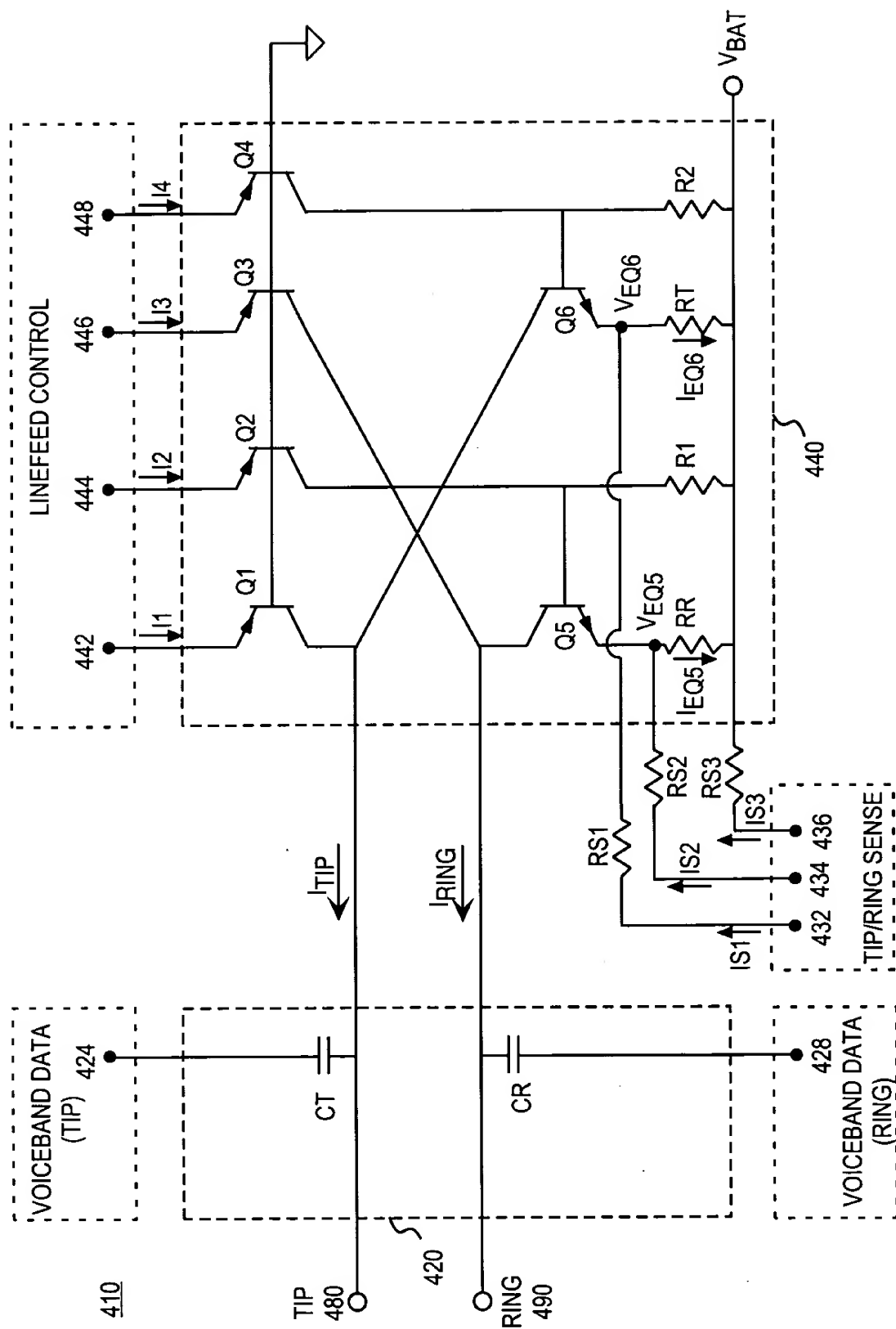


FIG. 4

TO SIGNAL PROCESSOR 210

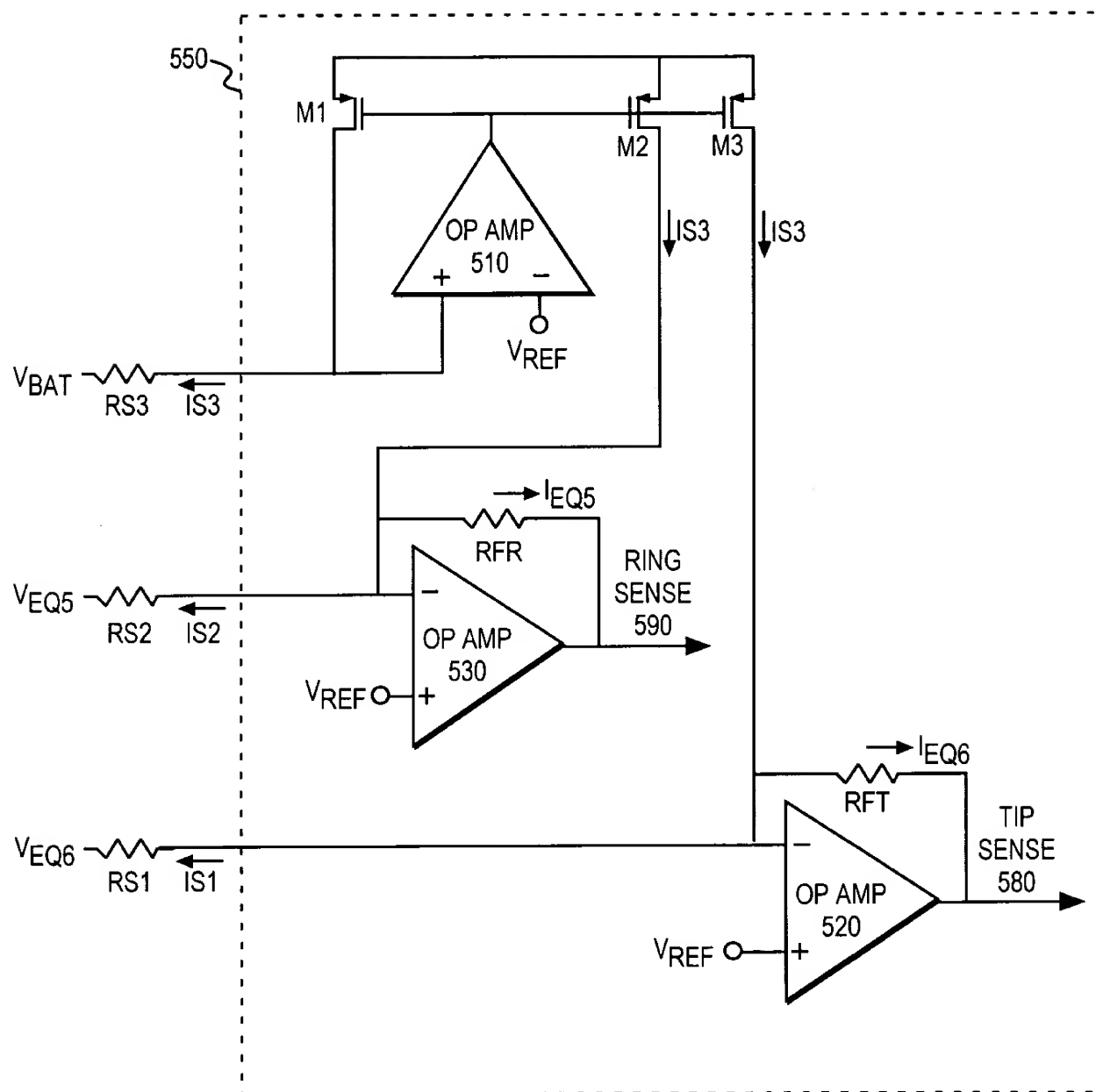


FIG. 5

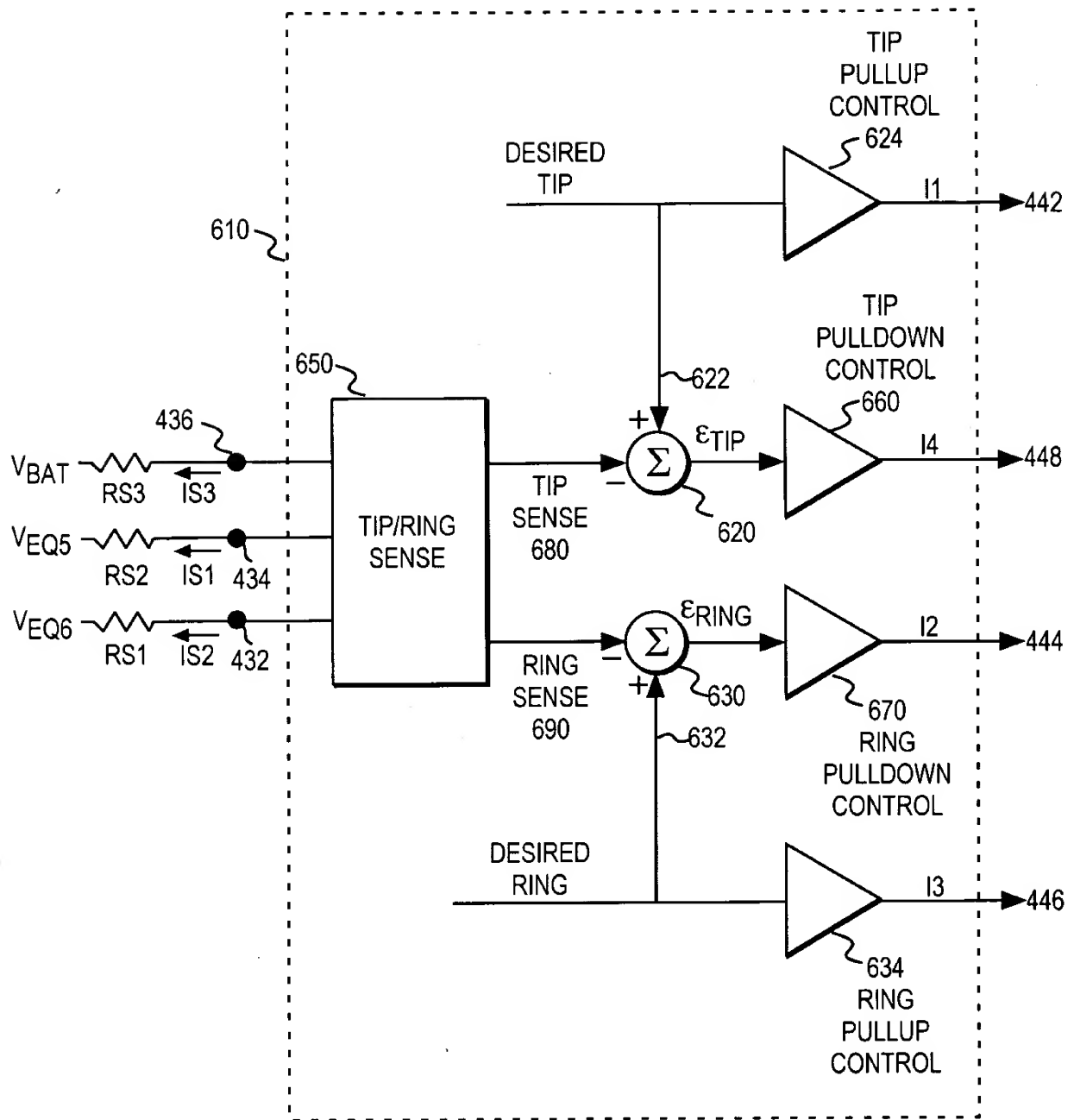


FIG. 6